



MCU OUTPUT STATE

Time	P0.0	P0.1	P0.2	P0.3	P0.4	P0.5	P0.6	P0.7
1	0	0	0	0	0	0	0	0
2	0	0	0	0	0	0	0	0
3	0	0	0	0	0	0	0	0
4	0	0	0	0	0	0	0	0
5	0	0	0	0	0	0	0	0
6	0	0	0	0	0	0	0	0
7	0	0	0	0	0	0	0	0
8	0	0	0	0	0	0	0	0
9	0	0	0	0	0	0	0	0
10	0	0	0	0	0	0	0	0
11	0	0	0	0	0	0	0	0
12	0	0	0	0	0	0	0	0
13	0	0	0	0	0	0	0	0
14	0	0	0	0	0	0	0	0
15	0	0	0	0	0	0	0	0
16	0	0	0	0	0	0	0	0

NOTE  
 1. RESISTANCE IS SHOWN IN OHM (Ω), AND 1M, 100K, 100Ω  
 2. UNLESS OTHERWISE NOTED IN SCHEMATIC ALL CAPACITOR  
 UNLESS LESS THAN 1 ARE COMPRESSED IN UP AND THE  
 UNLESS MORE THAN 1 IN pF